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SP605 Evaluation Board Overview The SP605
board enables hardware and software
developers to create or evaluate designs
targeting the Spartan®-6 XC6SLX45T-3FGG484
FPGA. The SP605 provides board features
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SP605 Reference Design User Guide

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February 14, 2019 This guide describes the clocking resources available in all Spartan-6 devices, including the DCMs and PLLs.

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and power-sensitive applications that require transceiver capabilities in one package. The SP605 evaluation kit provides integration of hardware, software, intellectual property (IP), and pre-verified reference designs so development can begin right out of the box.

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